

*B1
correct*

(5) counts the number of failure data in changing of data, and
 (6) determines that changing of data has been successfully completed if the number of failure data is not larger than a predetermined number, said predetermined number satisfying the condition that the predetermined number of failure data can be corrected.

29. The flash memory system according to claim 28, wherein said predetermined number is one.

*Could
B3*

30. The flash memory system according to claim 28, wherein a data length of each data stored in said memory unit is one bit.

31. A flash memory system comprising:
 a memory cell unit, including a plurality of flash memory cells, for storing data which can be electrically changed;
 wherein said flash memory system

(1) reads out the data stored in said memory unit together with a check code provided for detecting and correcting an error in the data,

(2) writes the data and the check code into said memory unit,

(3) counts the number of errors in the data and the check code after the write,

(4) issues an alarm if the number has exceeded a predetermined number, said predetermined number satisfying the condition that said predetermined number of errors can be correctable.

32. The flash memory system according to claim 31, wherein said predetermined number is one.

33. The flash memory system according to claim 31, wherein a data length of each stored in said memory unit is one bit.

A